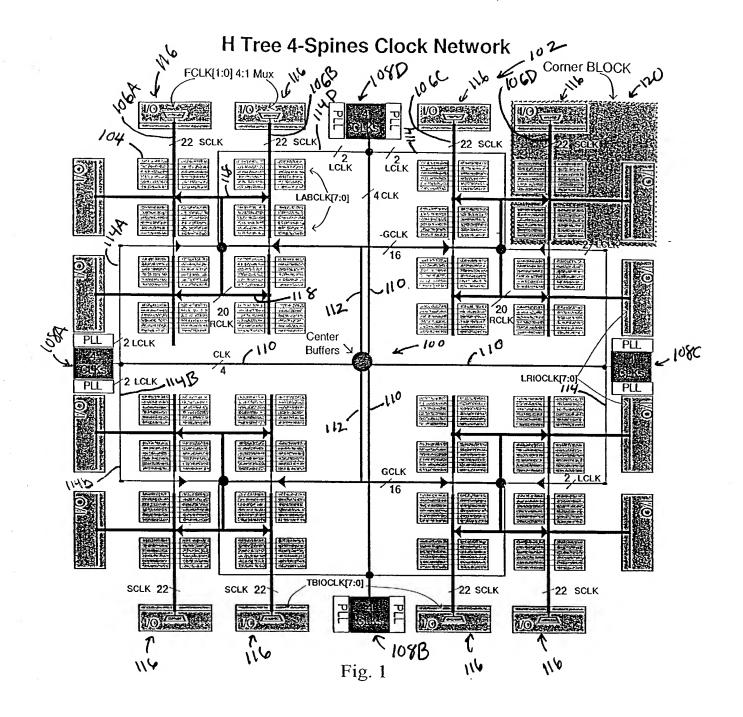
Inventor Triet NGUYEN, et al Application No 10/076,172 Docket No . 306812002000

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Docket No 306812002000
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## H Tree 2-Spines Clock Network

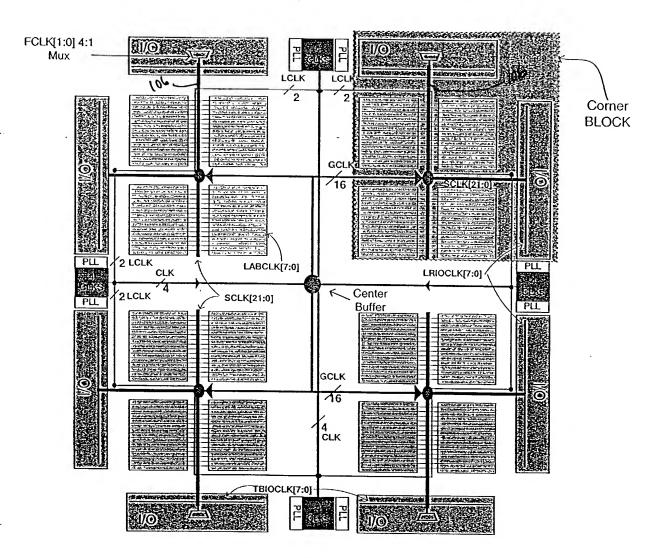


Fig. 2

### **CORNER BLOCK for 4-Spines Clock Network**

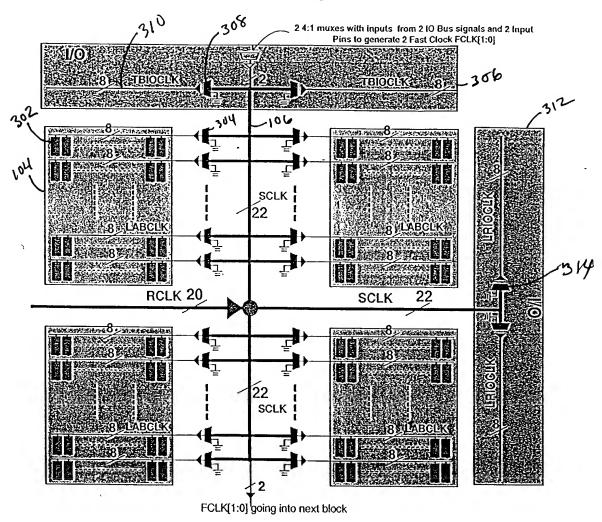


Fig. 3

### **CORNER BLOCK for 2-Spines Clock Network**

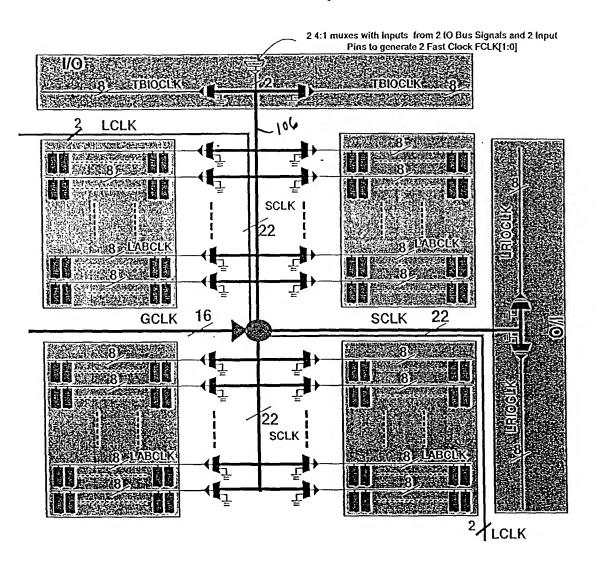


Fig. 4

#### **GLOBAL and LOCAL Clocks Generation**

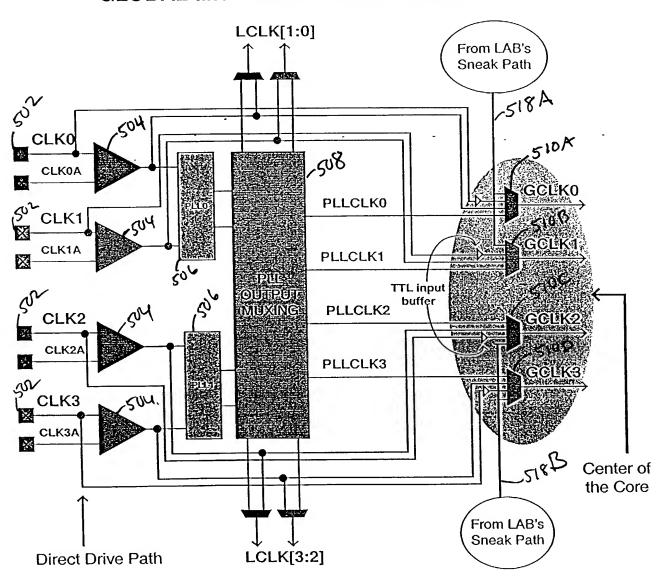


Fig. 5

#### Generation of Global Clocks GCLK[15:0]

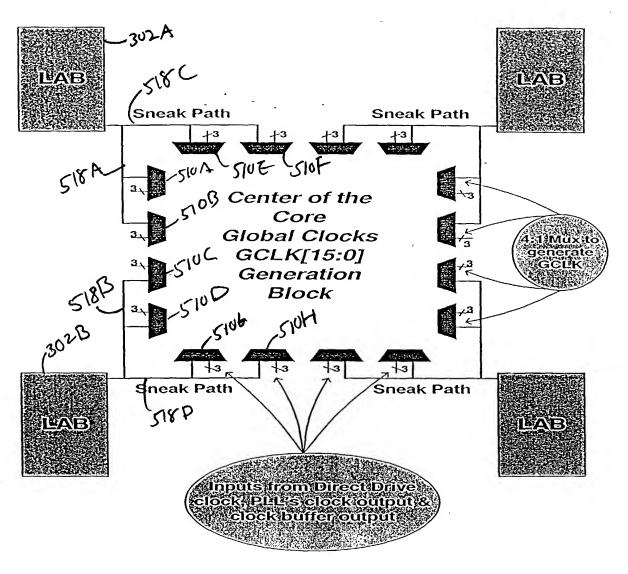


Fig. 6

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# Power Bus Segmentation for CLOCKs

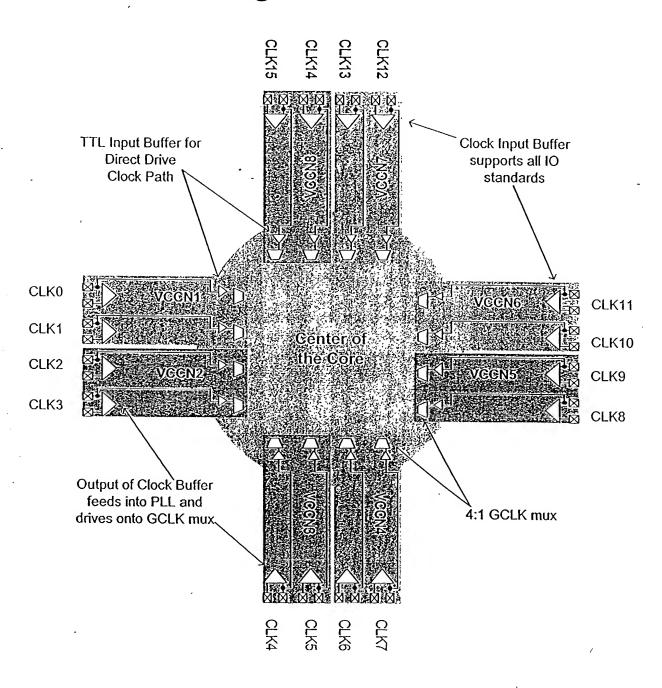


Fig. 7